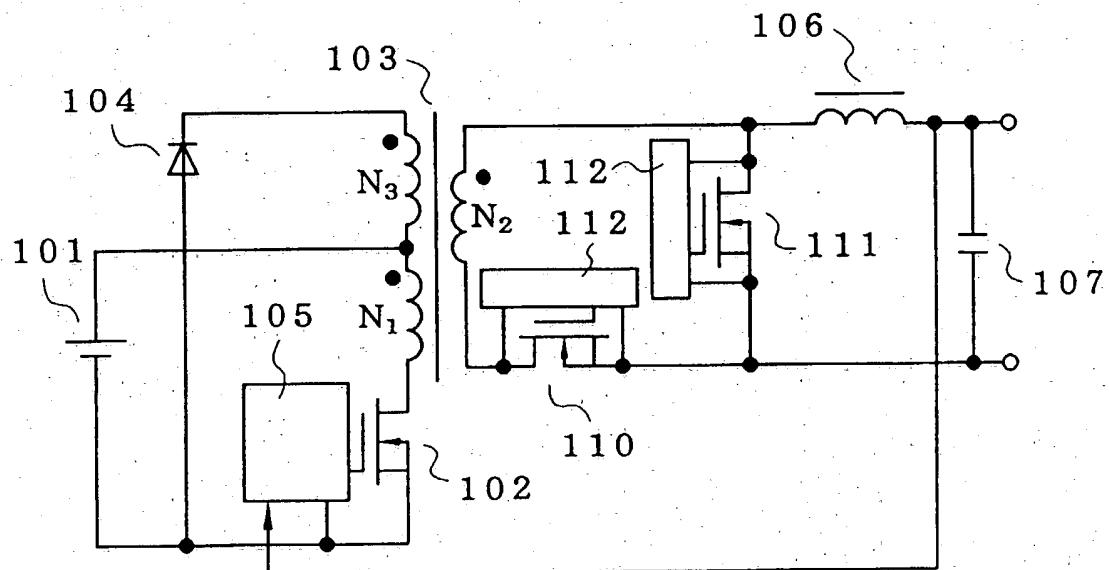


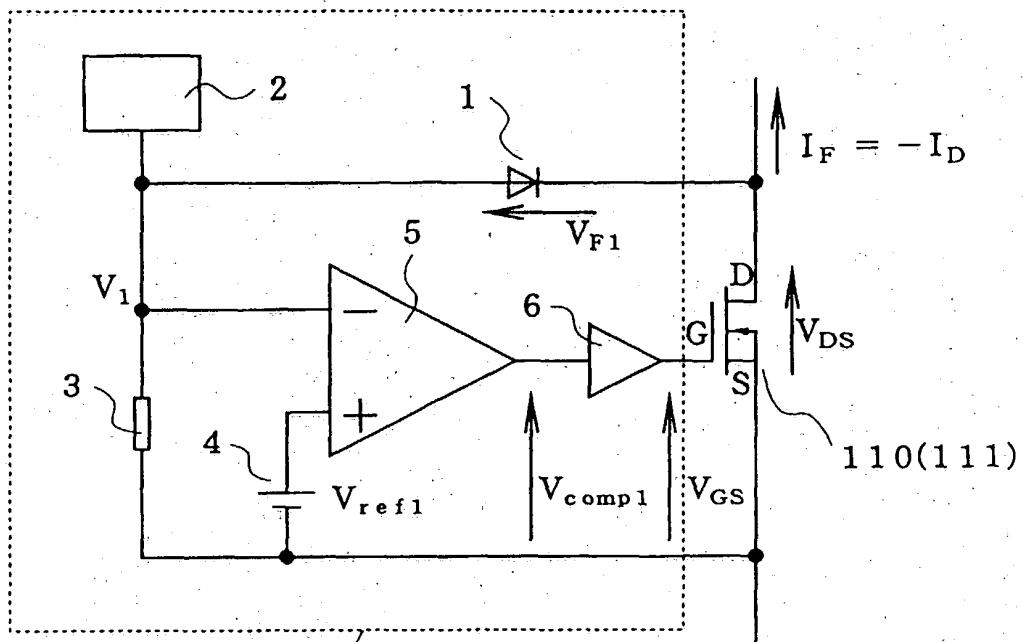
【書類名】図面

【図1】



110, 111 : MOSFET
112 : 制御回路 *Control circuit*

【图2】



112a

1: ダイオード Diode

2: 電流供給手段 Current supplying unit

3 : 抵抗 Resistor

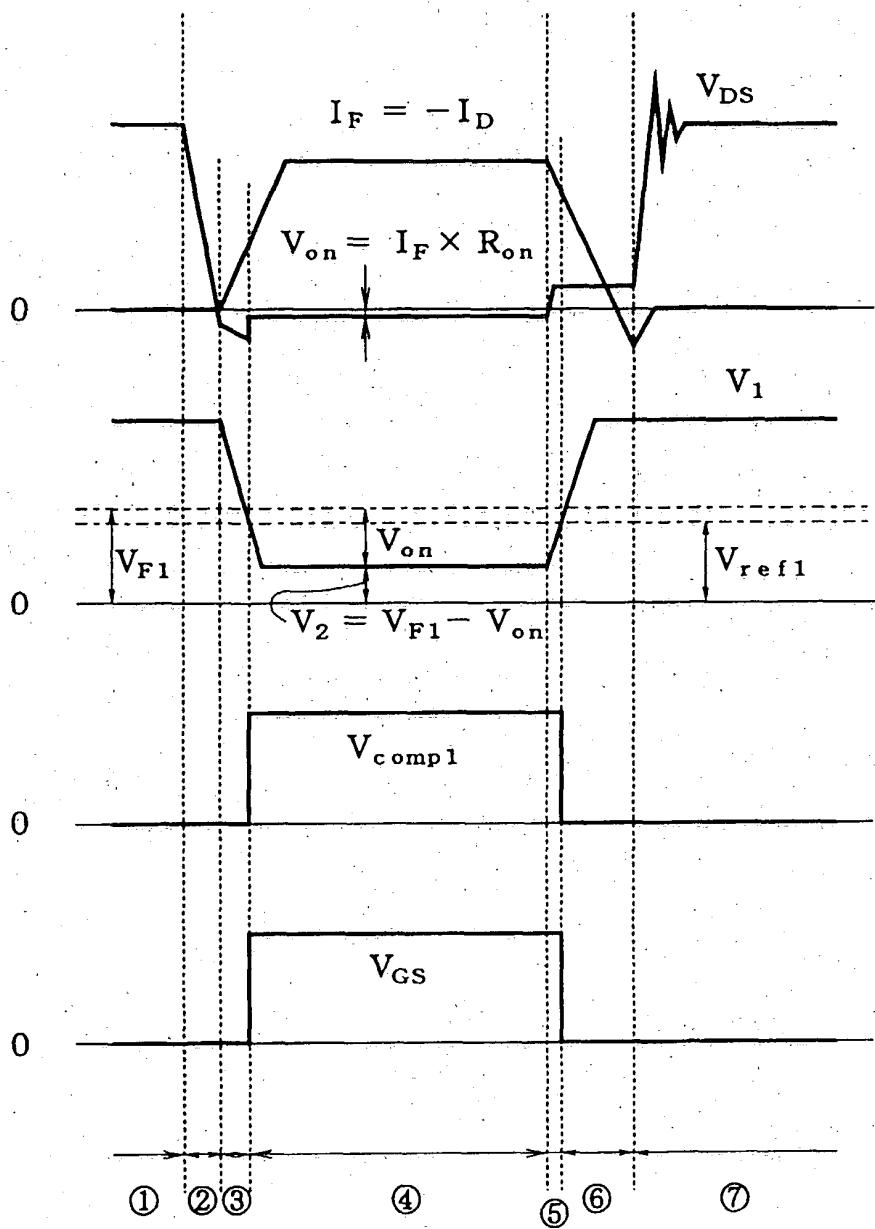
4. 基準電源 Reference voltage source

5：電壓比較手段 Voltage comparing unit

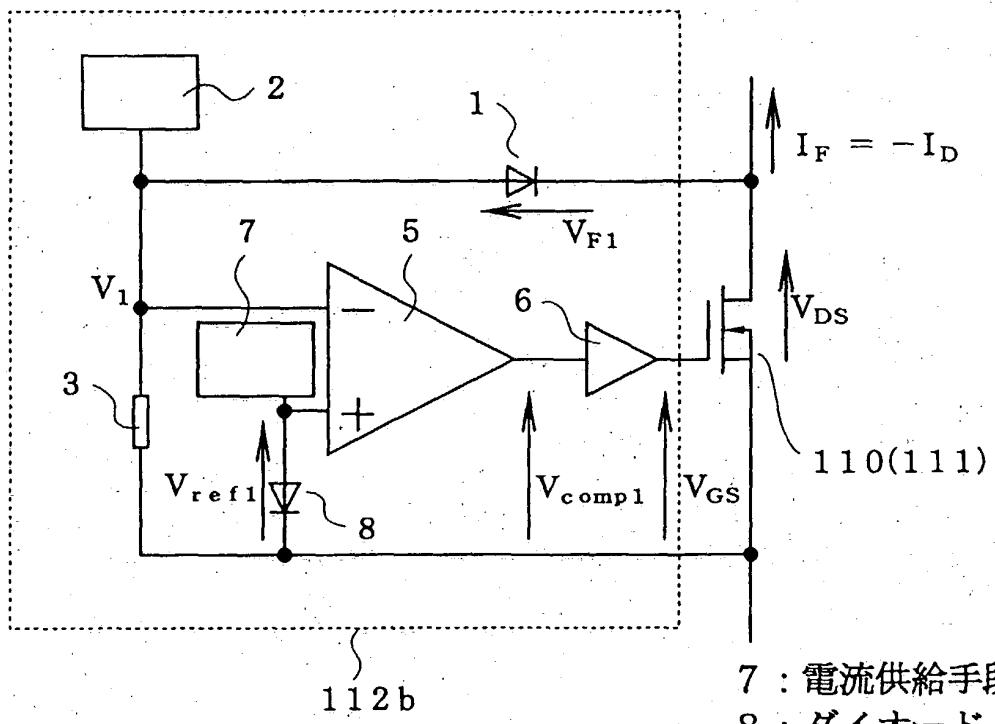
6：ゲート駆動手段 Gate driving unit

112a：制御回路 Control circuit

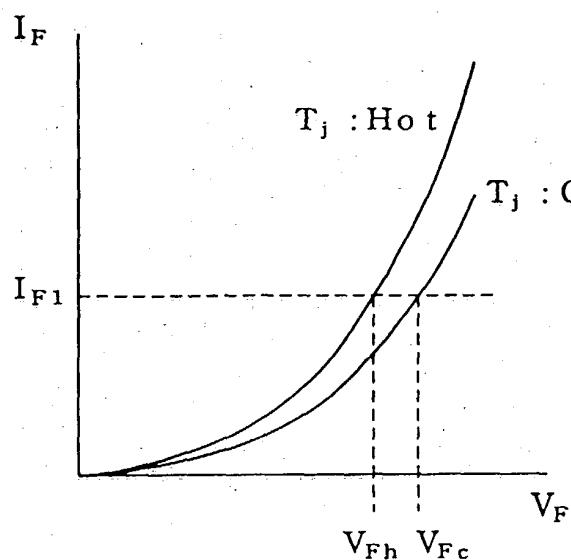
【図3】



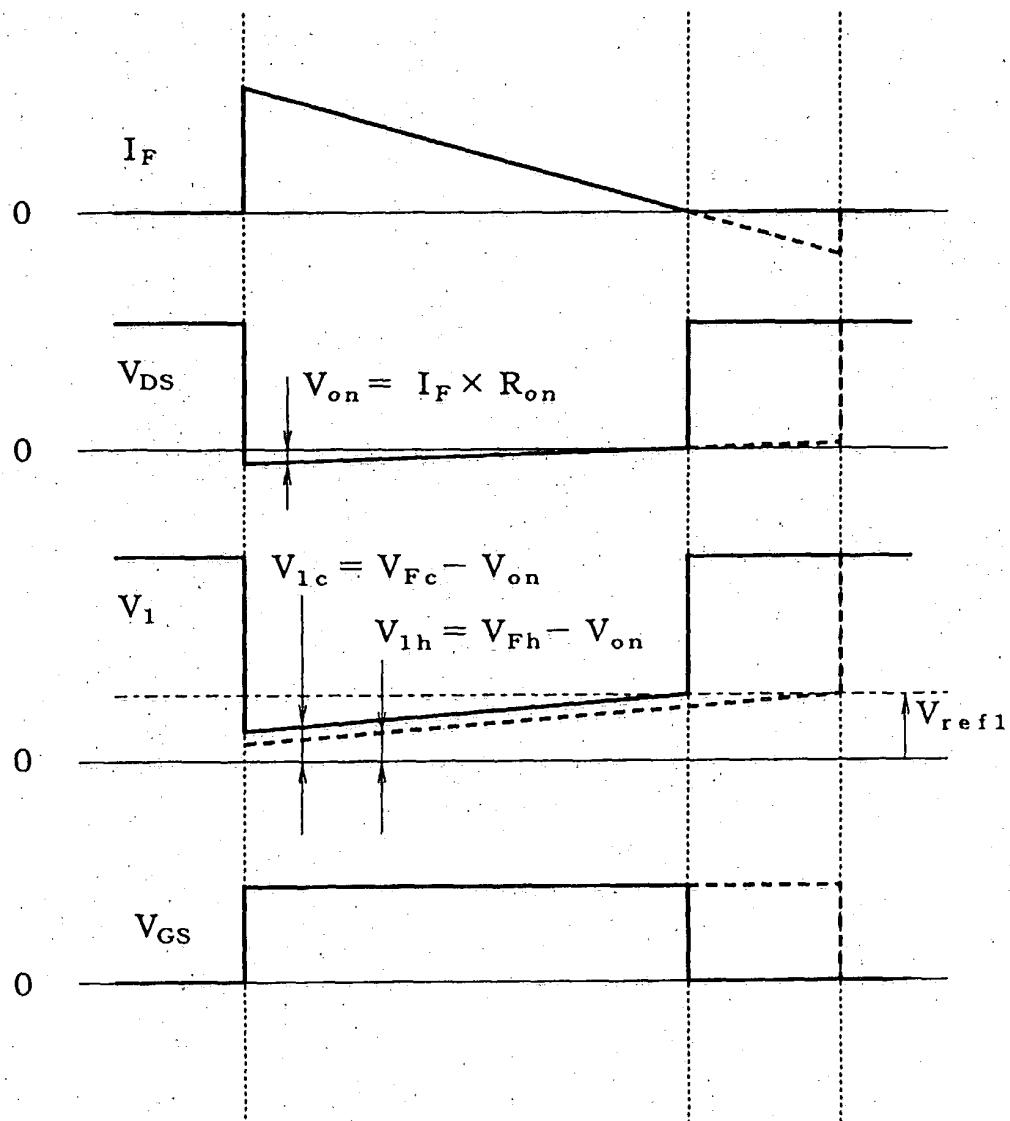
【図4】



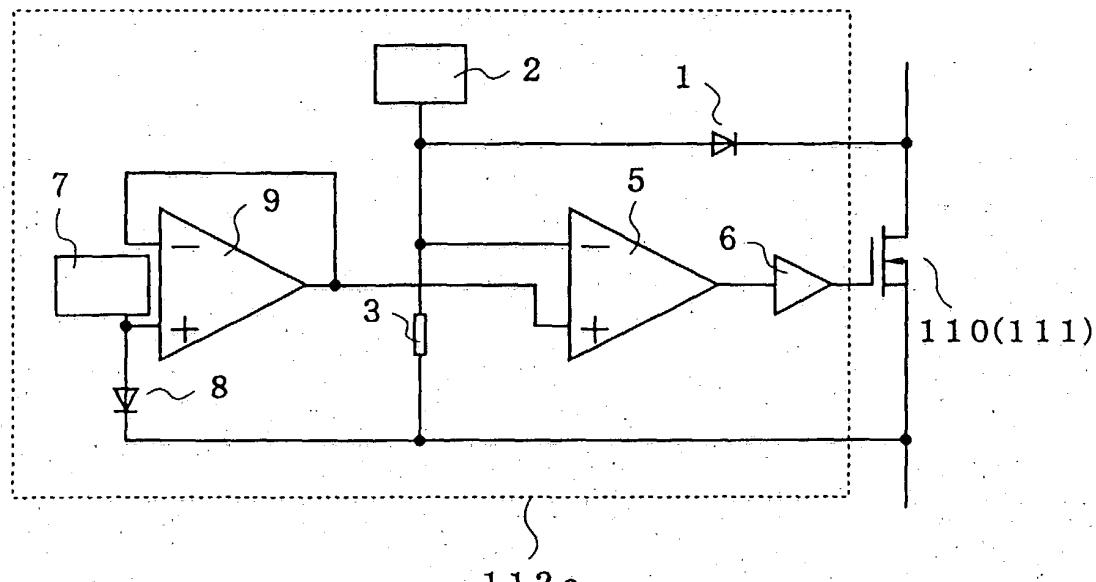
【図5】



【図6】

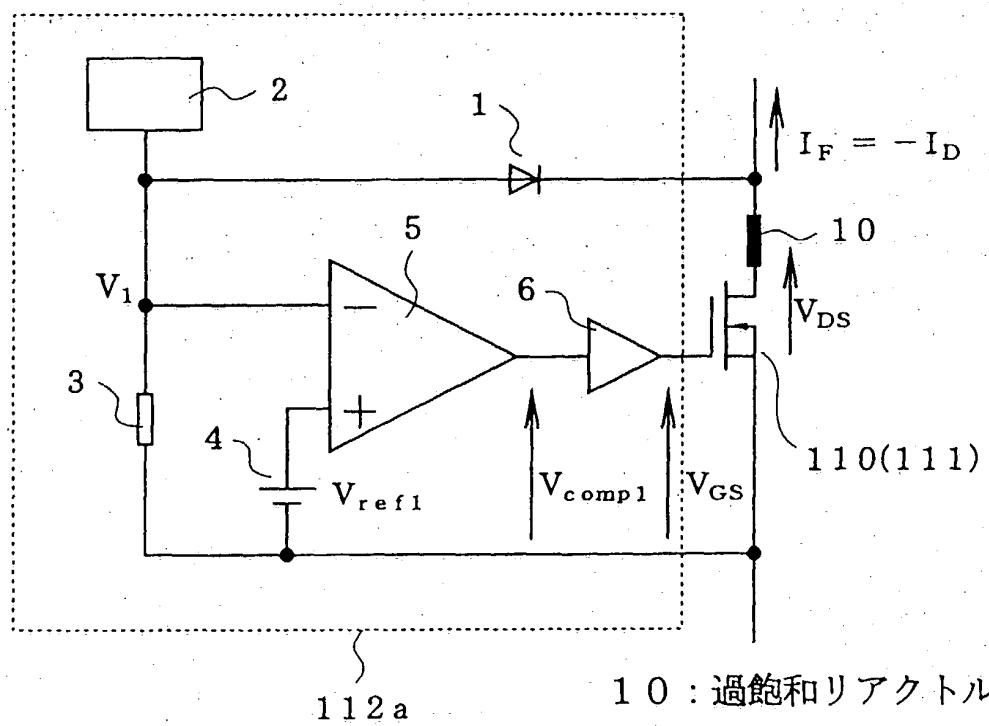


【図7】



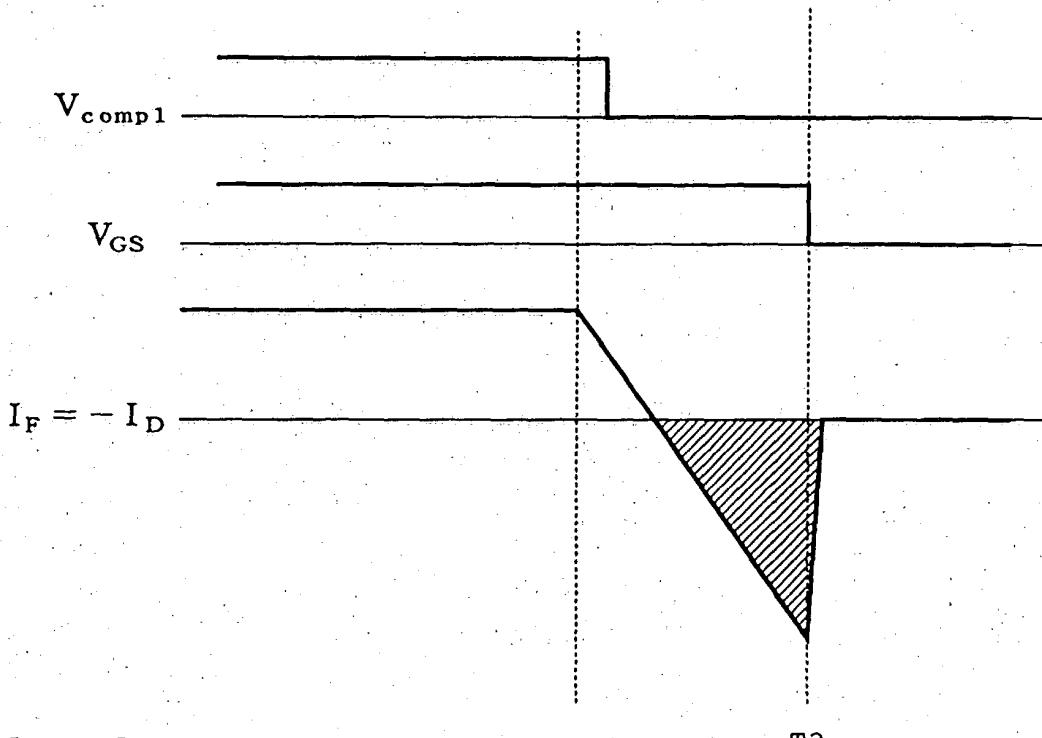
9: ボルテージバッファ Voltage buffer
112c: 制御回路 Control circuit

【図8】

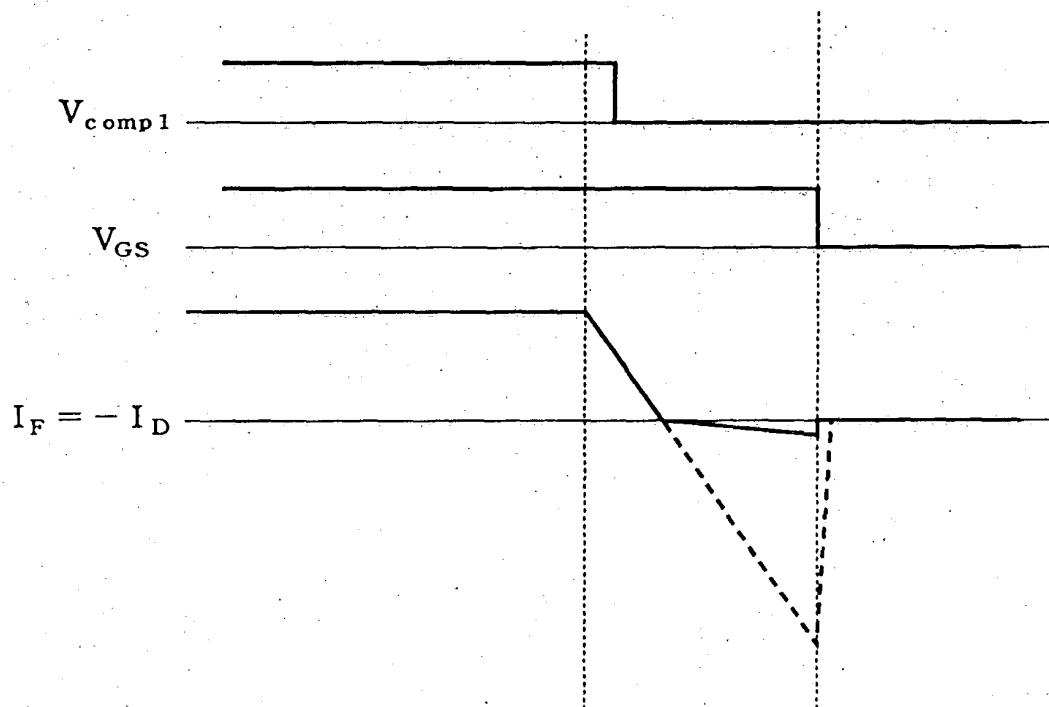


10: 過飽和リアクトル Saturable reactor

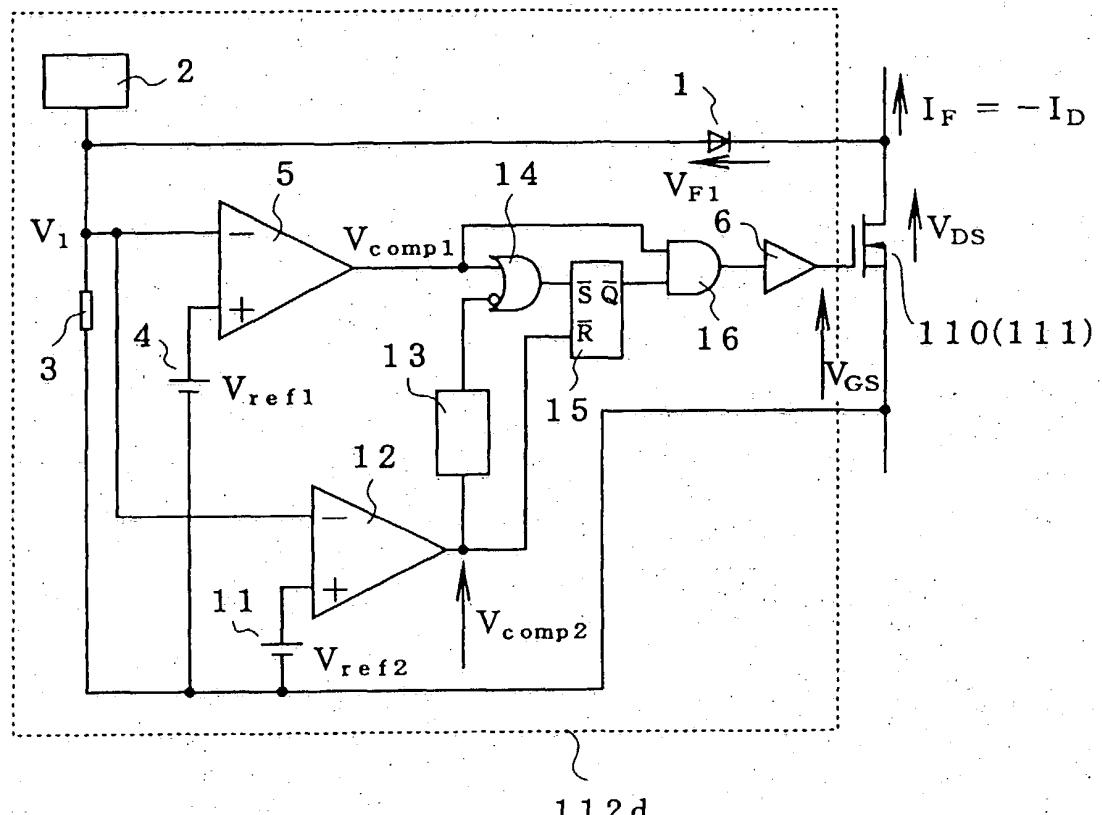
【図9】



【図10】

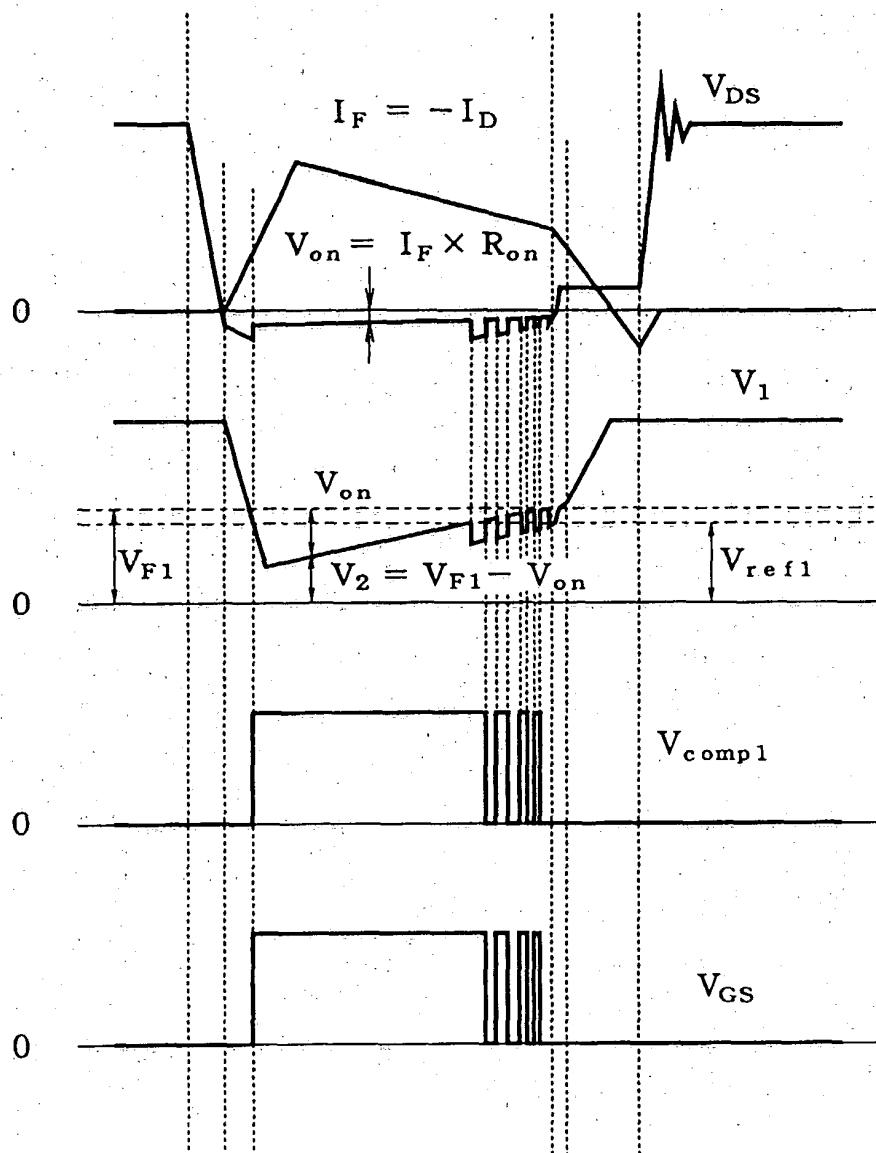


【図11】

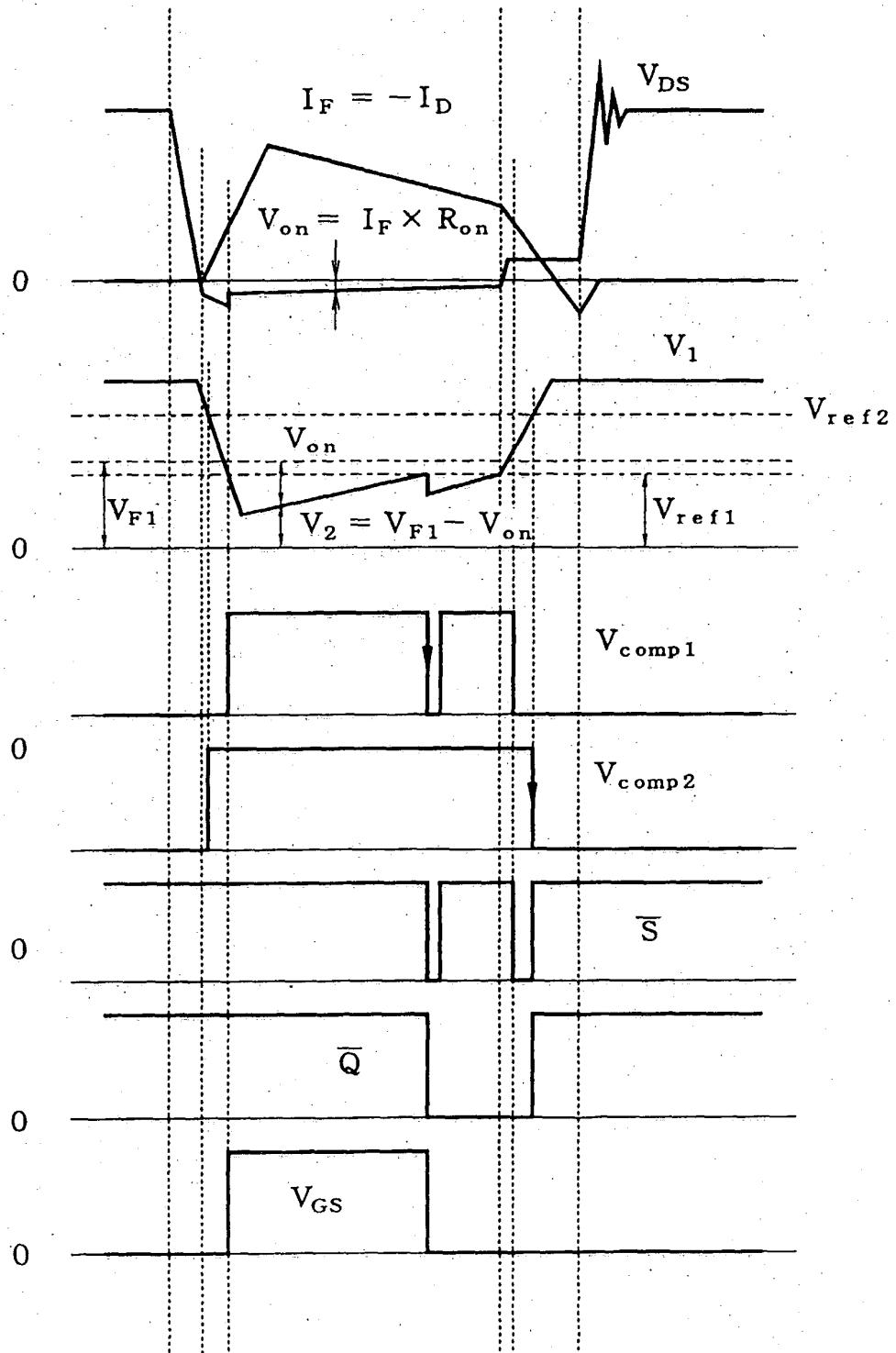


11: 基準電源 Reference voltage source
12: 電圧比較手段 Voltage comparing unit
13: 遅延回路 Delay circuit
14: OR回路 OR circuit
15: RSフリップフロップ RS flip-flop
16: AND回路 AND circuit
112d: 制御回路 Control circuit

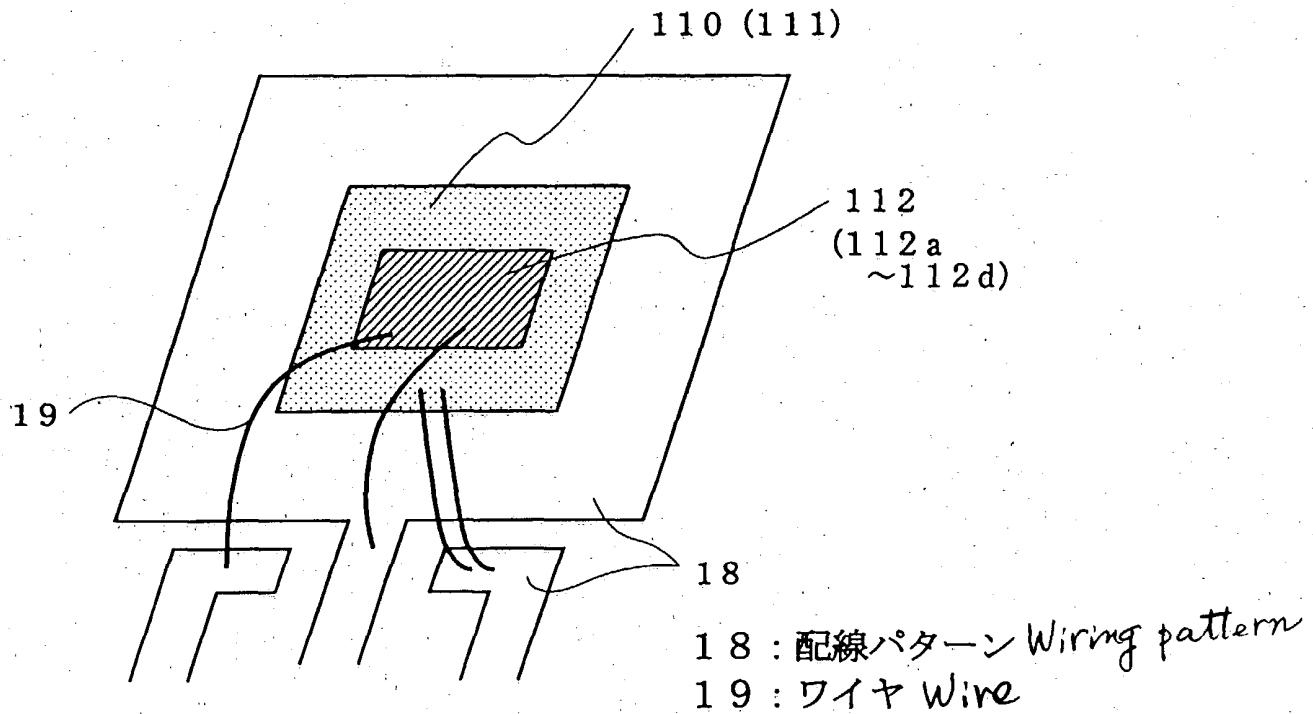
【図12】



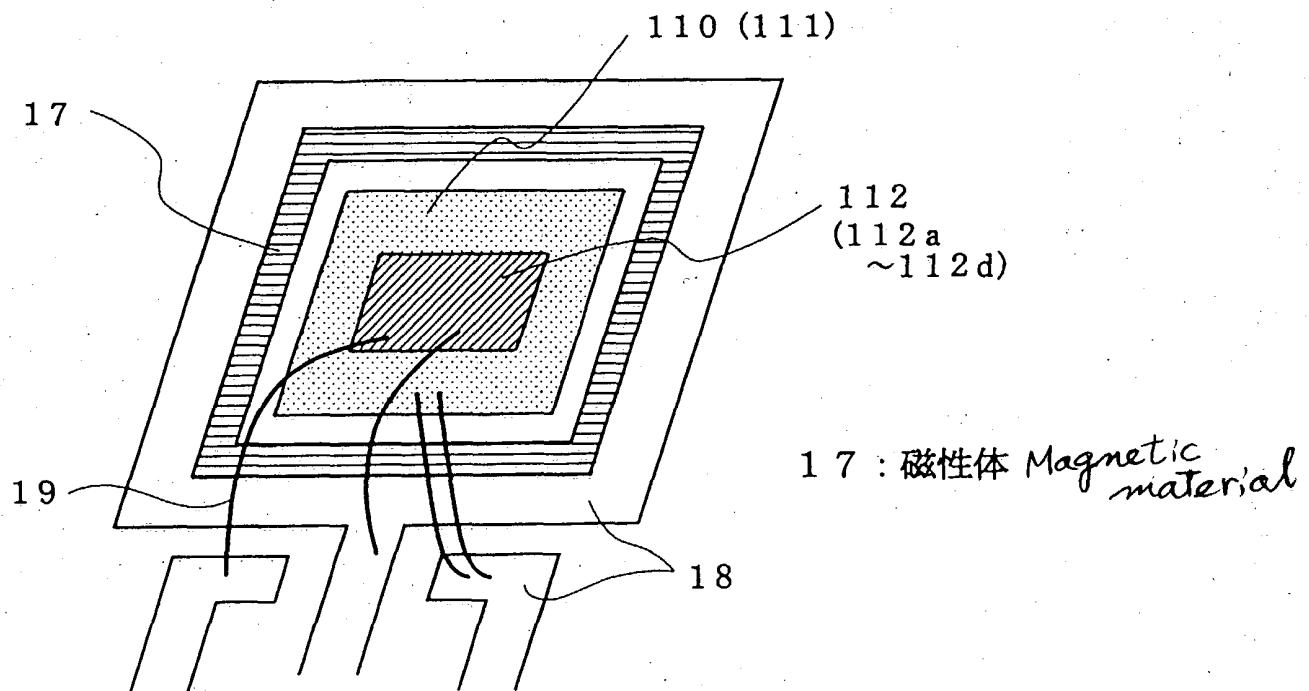
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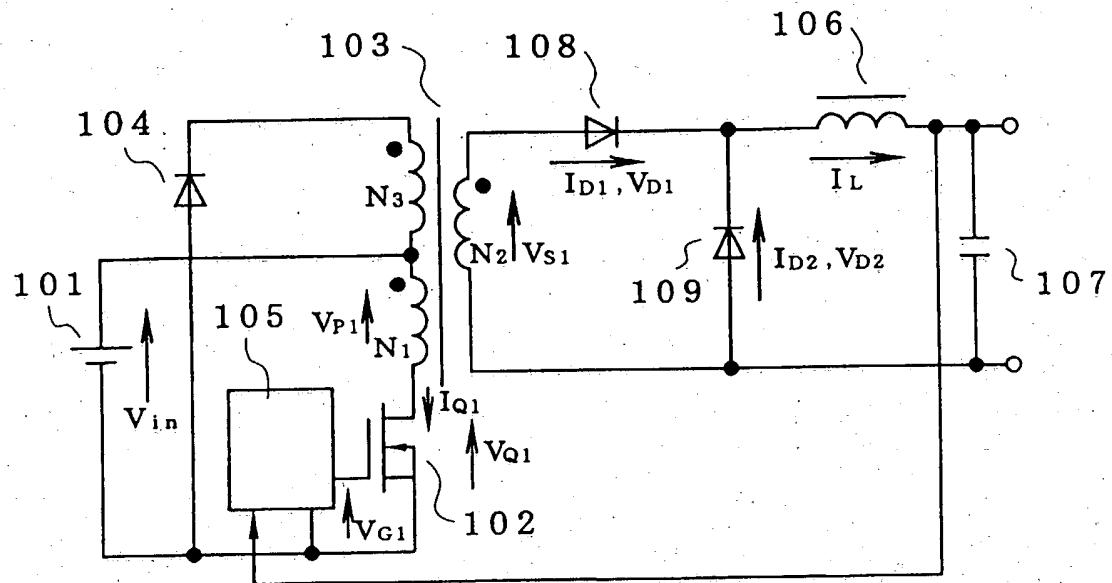
【図14】



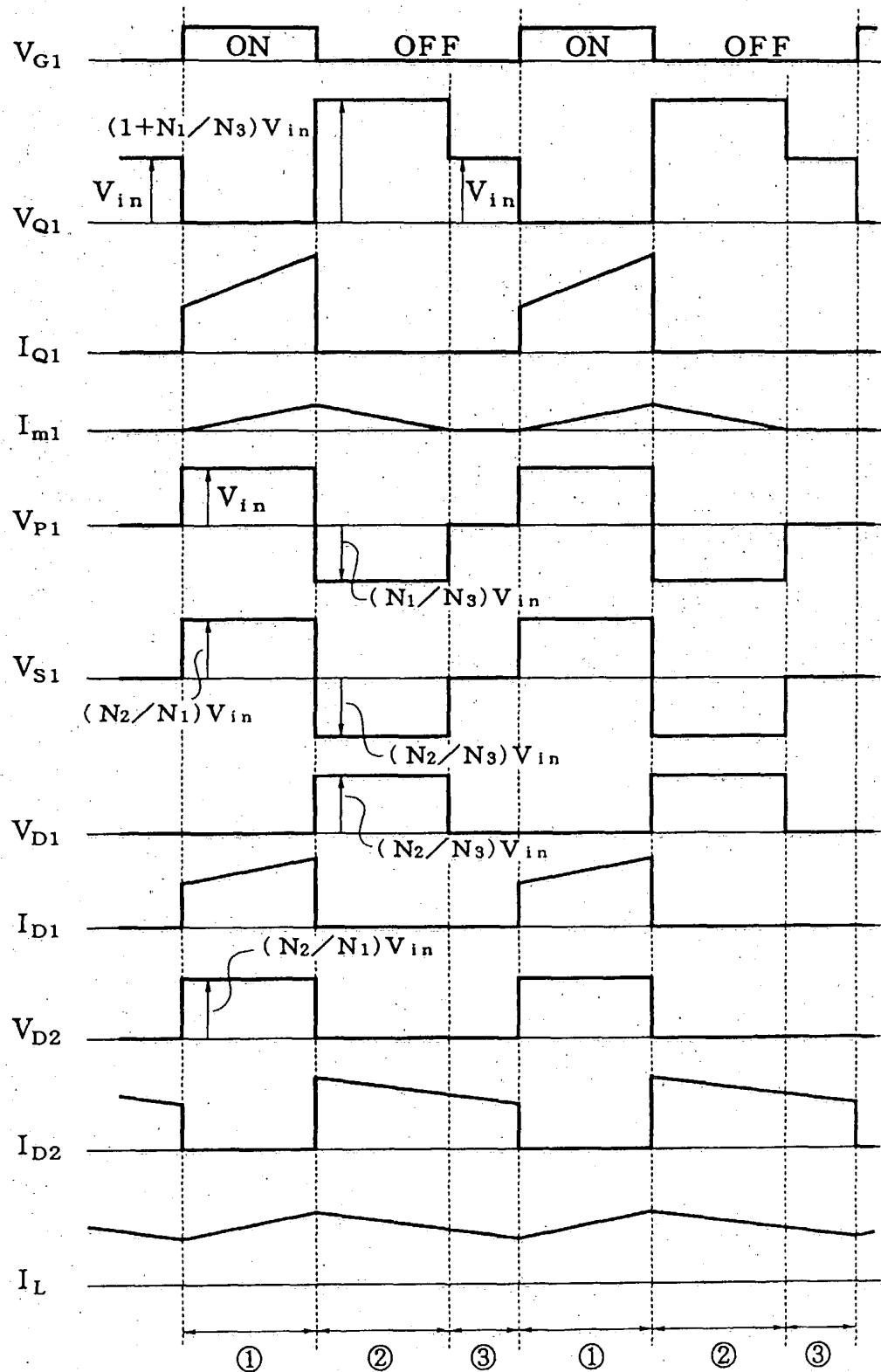
【図15】



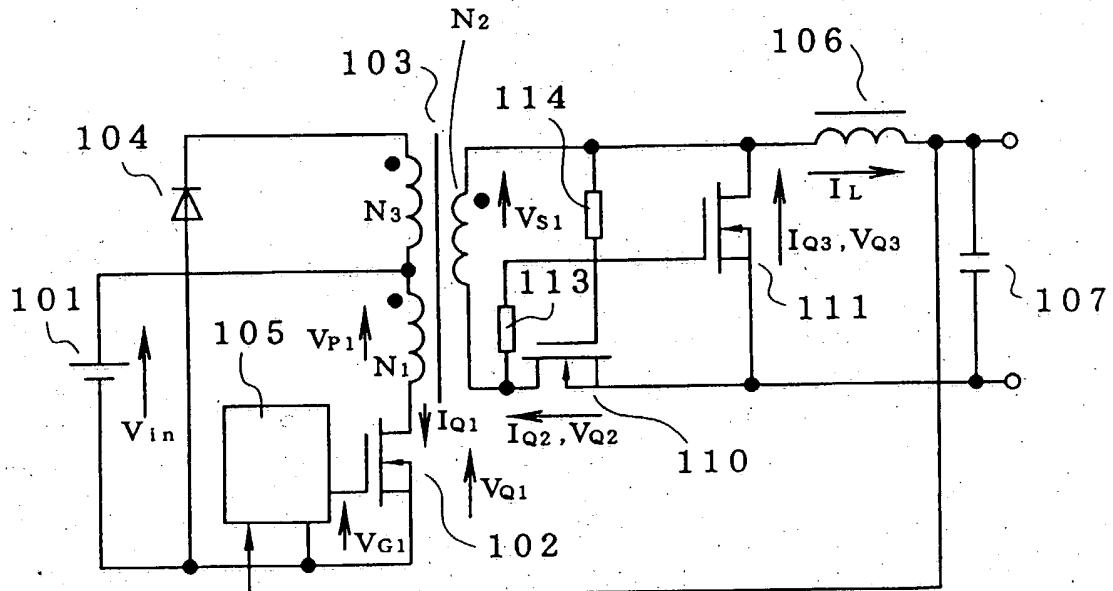
【図16】



【図17】



【図18】



〔図19〕

